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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Myeong-cheol Kim, et al.
Serial No.: 09/731,385
Filing Date: December 6, 2000
Title: SEMICONDUCTOR DEVICE HAVING SELF-ALIGNED CONTACT AND FABRICATING METHOD THEREFOR

Examiner: Nadav, O.
Group Art Unit: 2811

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

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Lisa Sanders

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Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT AFTER FINAL REJECTION

Sir:

This is in response to the final Office Action mailed on September 3, 2002. It is requested that the following amendments be entered and that the following remarks be considered.

Please amend the application as follows:

In the Claims

1. (Amended) A semiconductor device having a self-aligned contact, the semiconductor device comprising:
- a plurality of conductive patterns formed to be adjacent to one another by sequentially stacking and patterning a first conductive layer and a mask layer on a particular underlying layer;
 - a first insulation layer filling a gap between adjacent conductive patterns, the first insulation layer being formed of a first insulating material;
 - a second insulation layer having a spacer shape, the second insulation layer formed at the

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